

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/826693
				Filing Date	04/04/01
				First Named Inventor	MARIO D. NEMIROVSKY
				Group Art Unit	2188
				Examiner Name	REGINALD G. BRAGDON
Sheet	1	of	2	Attorney Docket Number	MIPS.0172-00-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
RGB		US-5708814	01-13-1998	SHORT ET AL.	
RGB		US-6460105	10-01-2002	JONES ET AL.	
RGB		US-20020049964	04-25-2002	TAKEYAMA ET AL.	
RGB		US-20020071393	06-13-2002	ENRIQUE MUSOLL	
RGB		US-6535905	03-18-2003	KALAFATIS ET AL.	
RGB		US-20010052053	12-13-2001	NEMIROVSKY ET AL.	
RGB		US-20020083173	06-27-2002	MUSOLL ET AL.	
		U.S. Patent Application Serial Number 09/608,750	NOT YET PUBLISHED	MUSOLL ET AL.	
RGB		US-4200927	04-29-1980	HUGHES ET AL.	
RGB		US-5784649	07-21-1998	BEGUR ET AL.	
RGB		US-6529515	03-04-2003	RAZ ET AL.	
RGB		US-6640248	10-28-2003	JORGENSEN, JACOB W.	
RGB		US-6625808	09-23-2003	TARDITI, DAVID R.	
RGB		US-6738371	05-18-2004	AYRES, LAWRENCE	
RGB		US-6738378	5-18-2004	TUCK, III; RUSSELL R.	
RGB		US-6813268	11-02-2004	KALKUNTE ET AL.	

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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
RGB	BA	WO-03/005645-A2	01-16-2003	SAMPATH ET AL.		

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RGB	AA	YAMAMOTO, WAYNE. <i>An Analysis of Multistreamed, Superscalar Processor Architectures</i> . University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
RGB	AB	YAMAMOTO et al. "Increasing Superscalar Performance Through Multistreaming." <i>Parallel Architectures and Compilation Techniques (PACT '95)</i> . 1995, pp 49-58.	
RGB	AC	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 nd Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
RGB	AD	MC68020 32-Bit Microprocessor User's Manual. 3 rd Ed.. 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
RGB	AE	POTEL, M. J. "Real-Time Playback in Animation Systems." <i>Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques</i> . 1977. pp. 72-77. San Jose, CA, US.	
RGB	AF	ARM Architecture Reference Manual. 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.	
	AG	ESA/390 Principles of Operation. IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.	
RGB	AH	MC88110 Second Generation RISC Microprocessor User's Manual. 1991. pp. 10-66, 10-67, and 10-71. Motorola, Inc.	
RGB	AI	DIEFENDORFF, KEITH ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." <i>IEEE Journal of Microelectronics</i> . April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.	
RGB	AJ	KANE, GERRY. <i>PA-RISC 2.0 Architecture</i> . 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
RGB	AK	DIEFENDORFF, KEITH ET AL. "Altivec Extension to PowerPC Accelerates Media Processing." <i>IEEE Journal of Microelectronics</i> . Vol. 20, No. 2 (2000): pp. 85-95	
RGB	AL	GRUNEWALD, WINFRIED ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." <i>Proceedings of EUROMICRO 22</i> , 1996. pp. 592-599	
RGB	AM	BRADFORD, JEFFREY ET AL. "Efficient Synchronization for Multithreaded Processors." <i>Workshop on Multithreaded Execution, Architecture, and Compilation</i> . Jan-Feb 1998. pp. 1-4	
RGB	AN	PAI, VIJAY ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." <i>Proceedings of ASPLOS-VII</i> , Oct. 1996: pp. 12-23, ACM, Inc.	
RGB	AO	YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53, IEEE.	
	AP	KESSLER, R. E. "The Alpha 21264 Microprocessor: Out of Order Execution at 600 MHz." August 1998.	
RGB	AQ	DONALDSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26 th Hawaii Conference on Systems Sciences. Vol. 1. 1993. Pages 448-456.	
RGB	AR	NEMIROVSKY ET AL. "DISC: Dynamic Instruction Stream Computer." ACM. 1991. Pages 163-171	
RGB	AS	MELVIN ET AL., "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
RGB	AT	SAMPATH ET AL., "Mechanism to Un-speculatively Pre-fetch Instructions from the Thread Associated to a Packet," April 2, 2001, Disclosure Document #491527, USPTO	
RGB	AU	MUSOLL ET AL, "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memory.", July 3, 2001, Disclosure Document #496391, USPTO	

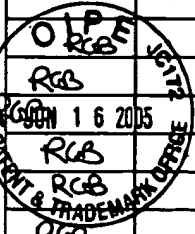
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U.S. PATENT DOCUMENTS					
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		6151644	11-21-2000	Chun-Chu Archie Wu	
		6453360	09-17-2002	Muller et al.	
		20010005253	06-28-2001	Hiroshi Komatsu	
		20010043610	11-22-2001	Nemirovsky et al.	
		20020016883	02-07-2002	Musoli et al.	
		20020054603	05-09-2002	Mussoli et al.	
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		20040172504	09-02-2004	Balazich et al.	
		20040015598	01-22-2004	Jin-Fun, Lin	
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RGB		5559970	09-24-1996	Vinod Sharma	
RGB		6614796	09-02-2003	Black et al.	

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RGB		6131163	10-10-2000	Scott L. Wiegel	
RGB		6016308	1-18-2000	Crayford et al.	
RGB		6502213	12-31-2002	Michel K. Bowman-Amuah	
RGB		5408464	04-18-1995	Mark Jurkevich	

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RGS		20040148382	07-29-2004	Nared et al.	
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RUB		6219783	04-17-2001	Zehlr et al.	
RUB		6253313	06-26-2001	Morrison et al.	
RUB		6047122	04-04-2000	Cynthia J. Spiller	
RUB		5892866	04-08-1999	Petrick et al.	
		09/591510	06-12-2000	Gellnes et al.	

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RGS		5675790	10-07-1997	Keith G. Walls	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ^{5 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

Examiner Signature	Reginald G. Bragdon	Date Considered	1/20/2006
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				Application Number	09/826693
				Filing Date	04/04/01
				First Named Inventor	Mario D. Nemirovsky
				Group Art Unit	2188
				Examiner Name	Reginald G. Bragdon
				Attorney Docket Number	MIPS.0172-00-US
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
RGS		6389468	05-14-2002	Muller et al.	
RGS		6650640	11-18-2003	Muller et al.	

FOREIGN PATENT DOCUMENTS						
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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

Examiner Signature	Reginald G. Bragdon	Date Considered	1/20/2006
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